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## NOTICE OF ALLOWANCE AND FEE(S) DUE

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09/04/2008

WENDEROTH, LIND & PONACK L.L.P. 2033 K. STREET, NW SUITE 800 WASHINGTON, DC 20006 EXAMINER

HERNANDEZ, NELSON D

ART UNIT PAPER NUMBER

2622

DATE MAILED: 09/04/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/774,466	02/10/2004	Shigetaka Kasuga	2004_0104A	9129

TITLE OF INVENTION: SOLID-STATE IMAGE SENSING APPARATUS

APPLN.	TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprov	isional	NO	\$1440	\$300	\$0	\$1740	12/04/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

### HOW TO REPLY TO THIS NOTICE:

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If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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WASHINGTON	I, DC 20006							(Depositor's name)
								(Signature)
								(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVEN	TOR		ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
10/774,466	02/10/2004		Shigetaka Kasuga	ı	•		2004_0104A	9129
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nonprovisional	NO	\$1440	\$300		\$0		\$1740	12/04/2008
EXAM	IINER	ART UNIT	CLASS-SUBCLASS	s				
HERNANDEZ	Z, NELSON D	2622	348-308000					
Change of correspondence address or indication of "Fee Address" (37 FR 1.363).  Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.			(1) the names of u or agents OR, alter (2) the name of a s registered attorney	a single firm (having as a member a ney or agent) and the names of up to ent attorneys or agents. If no name is				
PLEASE NOTE: Unl recordation as set fort (A) NAME OF ASSIG	less an assignee is ident h in 37 CFR 3.11. Comp GNEE		data will appear on the Tasubstitute for filing (B) RESIDENCE: (C	he pa g an a	tent. If an assigned assignment. and STATE OR CO	OUNT	RY)	up entity
<ul> <li>a. The following fee(s):</li> <li>Issue Fee</li> </ul>	are submitted:	40	<ul> <li>Payment of Fee(s): (</li> <li>A check is enclos</li> </ul>		se tirst reapply any	prev	iousiy paid issue fee s	nown above)
Publication Fee (No small entity discount permitted)			Payment by credit card. Form PTO-2038 is attached.  The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any					
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WENDEROTH,	LIND & PONACK I	HERNANDEZ, NELSON D			
2033 K. STREET,	NW		ART UNIT	PAPER NUMBER	
SUITE 800 WASHINGTON, I	DC 20006		2622 DATE MAILED: 09/04/200	8	

## Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 756 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 756 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)	
Notice of Allowability	10/774,466	KASUGA ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Nelson D. Hernández Hernández	2622	
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due course. <b>THIS</b>	
1. X This communication is responsive to Amendments filed on	<u>8/18/2008</u> .		
2. 🔀 The allowed claim(s) is/are <u>1-8,13-19 and 22-25 (Renumbe</u>	ered as 1-19 <u>)</u> .		
<ul> <li>3.  Acknowledgment is made of a claim for foreign priority ur</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> <li>3.  Copies of the certified copies of the priority documents</li> </ul>	been received. been received in Application No		
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements	
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give</li> </ol>			
5. CORRECTED DRAWINGS ( as "replacement sheets") mus	et be submitted.		
(a) $\square$ including changes required by the Notice of Draftspers	on's Patent Drawing Review ( PTO-	948) attached	
1) ☐ hereto or 2) ☐ to Paper No./Mail Date			
<ul><li>(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date</li></ul>	s Amendment / Comment or in the C	iffice action of	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in t			
6. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT			
Attachment(s)	5 Notice of Informal D	otant Application	
<ol> <li>Notice of References Cited (PTO-892)</li> <li>D Notice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>	<ul><li>5. ☐ Notice of Informal P</li><li>6. ☐ Interview Summary</li></ul>		
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<ol> <li>Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date</li> </ol>	7. Examiner's Amendn	letti Cottittetit	
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's Stateme	nt of Reasons for Allowance	
-	9. 🔲 Other		
	/Lin Ye/ Supervisory Patent Exa	aminer, Art Unit 2622	

## **DETAILED ACTION**

### Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on August 18, 2008 has been entered.

### Response to Amendment

2. The Examiner acknowledges the amended claims filed on August 18, 2008.

Claim 1 has been amended. Claims 9-12, 20 and 21 have been canceled.

## Allowable Subject Matter

- 3. Claims 1-8, 13-19, and 22-25 (Renumbered as 1-19) are allowed.
- 4. The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 1, the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, that the electric charge simultaneous removal unit includes timing generation circuits each having: a first switch transistor that serves as a switch; a

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capacitor disposed between a gate and a drain of the first switch transistor; and a second switch transistor, connected to the gate of the first switch transistor, wherein charging the capacitor prevents a voltage drop in the reset signal applied from a source of the first switch transistor of one of the timing generation circuits and in the readout signal applied from a source of the first switch transistor of another one timing generation circuits, and wherein the reset signal is applied to a gate of the reset transistor, and the readout signal is applied to a gate of the readout transistor to simultaneously reach a high level and maintain the high level for a time period, the reset transistor and the readout transistor being included in each of the photoelectric conversion circuits disposed in the region to be readout.

Regarding claim 13 (Renumbered as 11), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, an electric signal readout unit operable to read out the electric signals outputted from the photoelectric conversion circuits disposed in the region to be read out, wherein the electric signal readout unit includes: a first unit operable to output the activated reset signal to the each of the reset circuits in the photoelectric conversion circuits disposed in the region to be read out; and a second unit operable to output the activated readout signal to each of the reset circuits disposed in the region to be read out after outputting the reset signal, and wherein the first unit outputs the activated reset signal after the predetermined time in the electric charge accumulation unit has passed.

**Regarding claim 17** (Renumbered as 15), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest,

including all the elements of the present claim, an electric signal readout unit operable to read out the electric signals outputted from the photoelectric conversion circuits disposed in the region to be read out, wherein the electric signal readout unit includes: a first unit operable to output the activated reset signal to each of the reset circuits in the photoelectric conversion circuits disposed in the region to be read out; and a second unit operable to output the activated readout signal to each of the reset circuits disposed in the region to be read out after outputting the reset signal, and wherein the first unit outputs the activated reset signal before the predetermined time in the electric charge accumulation unit has passed.

Regarding claim 19 (Renumbered as 17), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, an electric signal readout unit operable to read out the electric signals outputted from the photoelectric conversion circuits disposed in the region to be read out, wherein the electric signal readout unit includes: a first unit operable to output the activated reset signal to each of the reset circuits in the photoelectric conversion circuits disposed in the region to be read out; and a second unit operable to output the activated readout signal to each of the reset circuits after outputting the reset signal, and wherein the first unit outputs the activated reset signal for a period starting from a mid point of the predetermined time until an end of the predetermined time in the electric charge accumulation unit.

**Regarding claim 22** (Renumbered as 18), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, that the electric charge simultaneous

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removal unit includes: a first switch transistor that serves as a switch; a capacitor disposed between a gate and a source or a drain of the first switch transistor; and a second switch transistor that serves as a switch, wherein the second switch transistor receives an all pixel reset switch signal and an all pixel reset signal, wherein the capacitor is charged during a time period in which the all pixel reset switch signal and the all pixel reset signal are input to a gate and a drain, respectively, of the second switch transistor, and wherein, when the capacitor is charged, the reset signal is input to the drain of the first switch transistor and is output simultaneously to all of the photoelectric conversion circuits from the source of the first switch transistor.

Page 5

Regarding claim 23 (Renumbered as 19), the main reason for indication of allowable subject matter is because the prior art fails to teach or reasonably suggest, including all the elements of the present claim, that wherein the electric charge simultaneous removal unit includes: a first switch transistor that serves as a switch; a capacitor disposed between a gate and a source or a drain of the first switch transistor; and a second switch transistor that serves as a switch, wherein the second switch transistor receives an all pixel reset switch signal and an all pixel reset signal, wherein the capacitor is charged during a time period in which the all pixel reset switch signal and the all pixel reset signal are input to a gate and a drain, respectively, of the second switch transistor, and wherein, when the capacitor is charged, the reset signal is input to the drain of the first switch transistor and is output simultaneously to all of the photoelectric conversion circuits from the source of the first switch transistor.

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### Contact

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nelson D. Hernández Hernández whose telephone number is (571)272-7311. The examiner can normally be reached on 9:00 A.M. to 5:30 P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lin Ye can be reached on (571) 272-7372. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nelson D. Hernández Hernández Examiner Art Unit 2622

NDHH August 28, 2008

> /Lin Ye/ Supervisory Patent Examiner, Art Unit 2622